

## **REMARKS**

Reconsideration and withdrawal of all grounds of rejection, and allowance of the pending claims are respectfully requested in light of the amendments and remarks made herein.

Claims 1-3, 10-14 and 17 stand objected to because of informalities. In response, claim 1-3, 10-14 and 17 have been amended as suggested by the Examiner. Accordingly, applicant requests removal of these objections.

Applicant greatly appreciates the Examiner's indication that claims 1-3, 10-14 and 17 would allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. However, applicants believe that since claims 1-3, 10-14 and 17 have been amended to obviate the objections as noted above that these claims are allowable.

Claim 6 stands rejected under 35 USC 103(a) as being unpatentable by Yamamoto. (U.S. Patent No. 6,831,705 B2) in view of Sakai et al. (U.S. Patent No. 4,654,884).

As indicated in the Office Action, Yamamoto fails to disclose determining whether a DC voltage level of an automatic gain control signal has a first value indicating that the automatic gain control is inactive or has a value within a predetermined range

indicating that the automatic gain control system is active. The addition of Sakai fails to cure the infirmities of Yamamoto.

The Office Action indicates that the above limitations at disclosed in Sakai in col. 8, line 1 – col. 9, line 11. Applicants respectfully disagree. Sakai teaches a level detector circuit 26 to extract a portion of an intermediate-frequency signal from a front stage in the intermediate-frequency amplifier circuit 12 to detect the level of a received signal. See col. 6, lines 24-27. The controller 28 uses the output of detector circuit 26 to enable the tuner 11 to receive the desired frequency..., see col. 8, lines 35-38, and . Although, the controller 28 utilizes the detected signal level to de-energize the AGC circuits or leave them un-changed, it does not make a determination as to whether the AGC circuits are active or inactive. Instead, it makes a determination whether the levels stored in the memory 28a meet certain relationships, and sets the intermodulation interference data to various states, see col. 8, line 55 to col. 9, line 22. Applicant can find nothing that teaches to determine a *DC* voltage level of an automatic gain control signal ... indicating that the automatic gain control system is *active or inactive*, as claimed in claim 6.

Having shown that the combined device resulting from the teachings of the cited references does not include all the elements of the present invention, Applicant submits that the reasons for the Examiner's rejections of the claims have been overcome and can no longer be sustained. Applicant respectfully requests reconsideration, withdrawal of the rejection and allowance of the claims.

For all the foregoing reasons, it is respectfully submitted that all the present claims are patentable in view of the cited references. A Notice of Allowance is respectfully requested.

Respectfully submitted,

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A handwritten signature in black ink, appearing to read "Thomas J. Onka", written over a horizontal line.

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